EVENT REPORT

Title of the Event: One Week Short Term Training Program on “Analog IC Design Using Cadence Tools”
Dates: 26th June 2023 – 1st July 2023
Organized by: Dr. G. Amanath & Dr. D. Rupa Kumar
Department: Electronics and Communication Engineering (ECE)
Institution: Marri Laxman Reddy Institute of Technology and Management (MLRITM)

The Department of Electronics and Communication Engineering at MLRITM organized a One Week Short Term Training Program (STTP) on “Analog IC Design Using Cadence Tools” from 26th June to 1st July 2023. The program aimed to enhance the professional skills of faculty members and research scholars in the field of VLSI design with a focus on Analog integrated circuits, simulation methodologies, and industry-standard design workflows.

The training covered fundamental to advanced concepts in Analog IC design including CMOS design principles, current mirrors, differential amplifiers, operational amplifiers, and layout design considerations. Expert trainers from academic and industrial backgrounds delivered interactive sessions demonstrating Cadence EDA tools used in semiconductor design and verification processes.

Hands-on laboratory sessions provided extensive practical exposure to circuit schematic development, SPICE simulation, design verification, and layout-versus-schematic (LVS) checks. Participants also explored performance optimization techniques for real-world Analog circuits including noise, power, and speed analysis aspects.

Throughout the program, the participants actively engaged in technical discussions, problem-solving activities, and design assignments that strengthened their domain knowledge and encouraged innovation in IC design applications. The event emphasized the growing demand for skilled VLSI engineers in the semiconductor industry and encouraged faculty to integrate these advancements into teaching and research.

The STTP concluded with a valedictory ceremony, where certificates were awarded to the participants. The organizers expressed sincere thanks to the management, resource persons, technical staff, and the ECE department team for their valuable support in making the program successful and impactful.

Overall, this Short-Term Training Program provided an excellent platform to upgrade technical expertise in VLSI and Analog IC Design, supporting the institution’s vision of empowering educators and enhancing competency for future electronic design technologies.